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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

DEC 19 2005

Applicants: Harinath B. Kamepalli, Padmaraj Sanjeevarao, Chang-Jin Park  
Assignee: Sun Microsystems, Inc.  
Title: Scan Chain Verification Using Symbolic Simulation  
Serial No.: 10/790,650 Filed: March 1, 2004  
Examiner: Paul Dinh Group Art Unit: 2825  
Docket No.: SUN040292 Customer No.: 33438

Austin, Texas  
December 19, 2005

COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the Office Action dated October 27, 2005, having a shortened statutory period expiring January 27, 2006. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

OK to enter  
PD  
1/4/06